

**Amendments to the Specification:**

Please replace paragraph [0039] with the following amended paragraph:

For this embodiment, Frame Control FEC encoding logic 340 includes an encoder 341 and/or a frame control interleaver 342. Encoder 341 may be adapted to encode twenty-five (25) control bits into a 100-bit code word. Frame control interleaver 342 redundantly maps the 100-bits into four (4) symbols of up to 84-bits each. Such operations are described on pages 9-12 of the HomePlug 1.0 Specification ~~incorporated herewith by reference.~~

Please replace paragraph [0040] with the following amended paragraph:

In addition, during ROBO mode, incoming data 331 to be transmitted from the HomePlug compliant station 110<sub>x</sub> is processed by different logic referred to as a "Data FEC encoding logic" 350. Data FEC encoding logic 350 is adapted to perform scrambling, Reed-Solomon encoding, convolutional encoding and complex bit interleaving operations on data 331, and thereafter, to load the encoded and/or interleaved data within payload 330. Such operations are described on pages 13-18 of the HomePlug 1.0 Specification ~~incorporated herewith by reference.~~